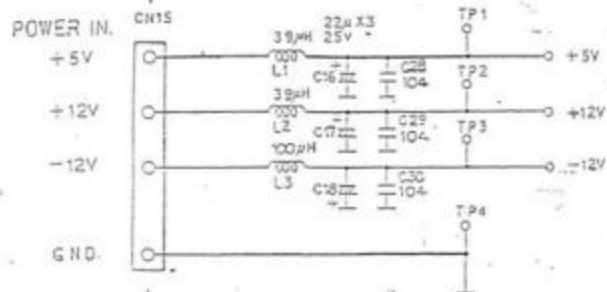


LOGIC ASSY



CIRCUIT SYMBOLS

 DIGITAL GROUND

 ANALOG GROUND

 FERRITE BEAD (L_F)

 +5V PULL-UP RESISTOR NETWORK

原本 MSX2

CPC-300(DOMESTIC)

APPROVAL

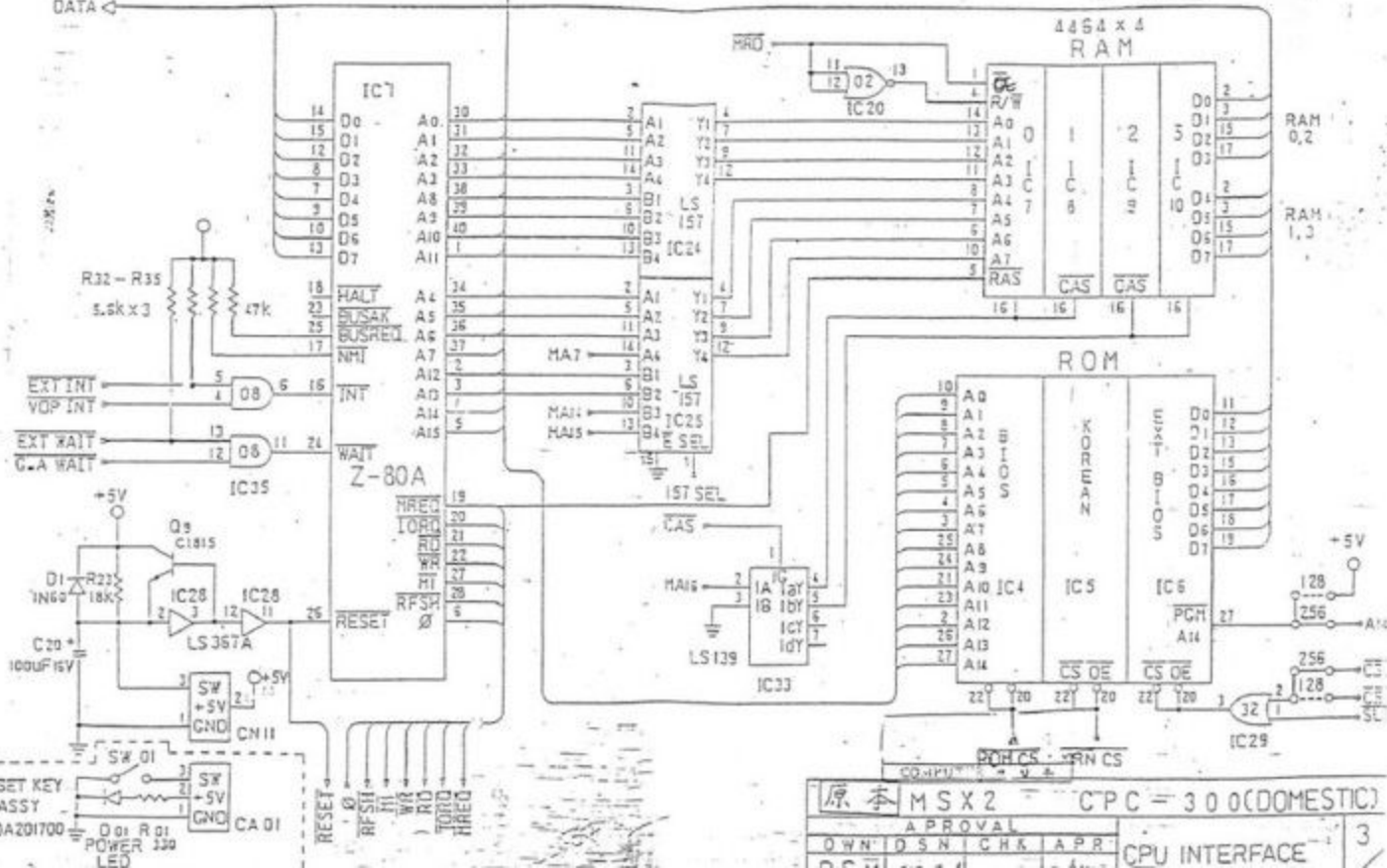
OWN LOSN CHA APR

LOGIC POWER

2/

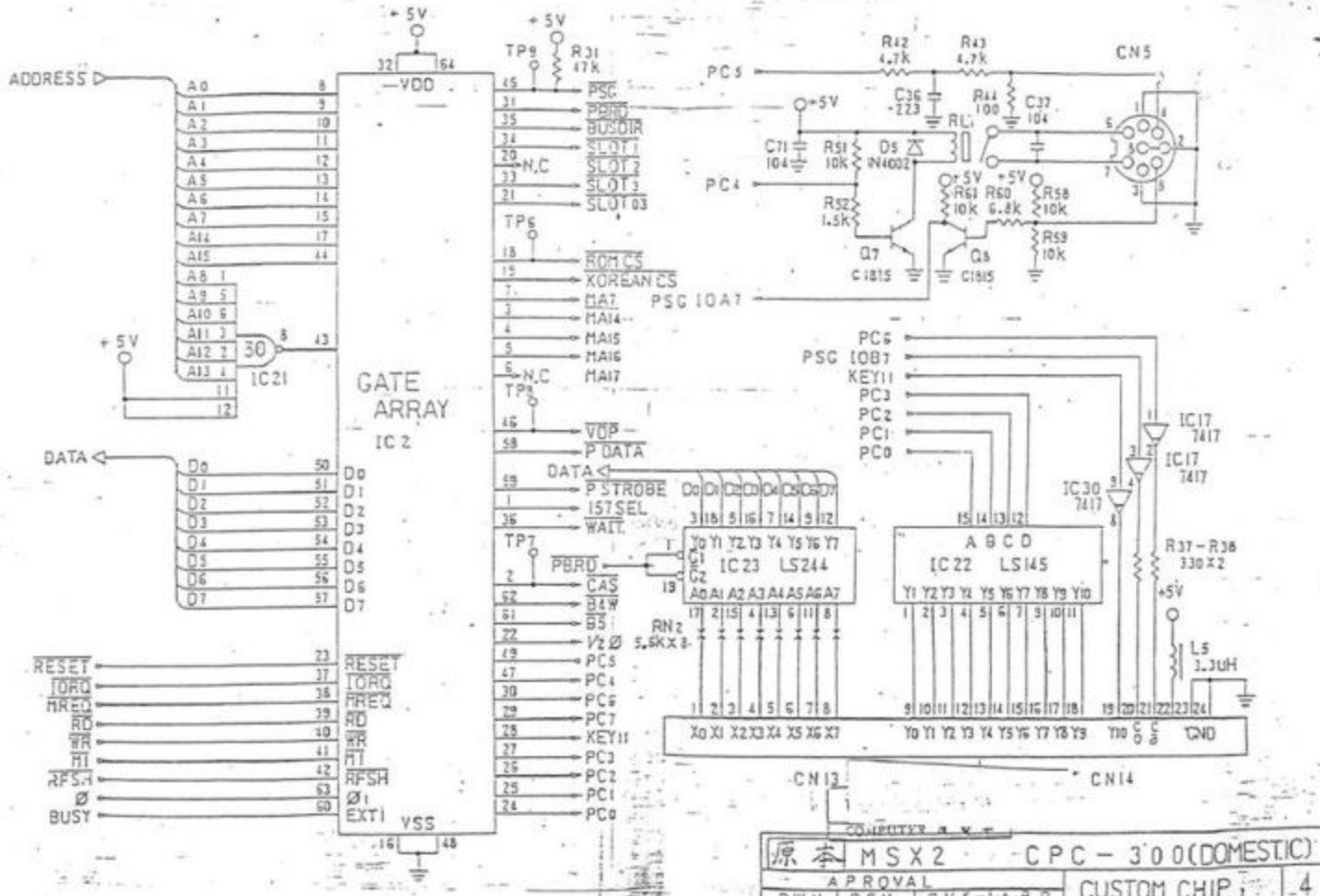
ADDRESS

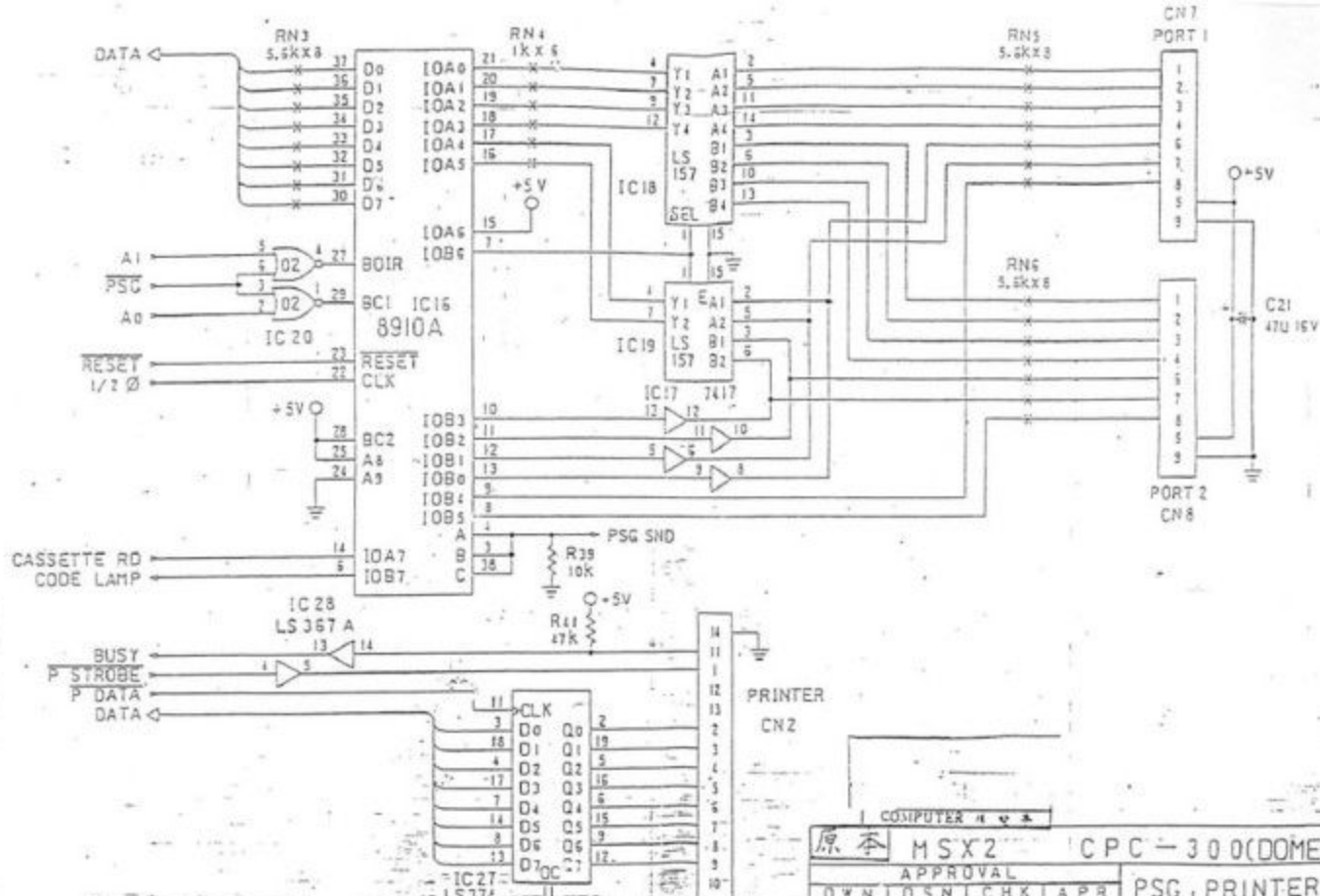
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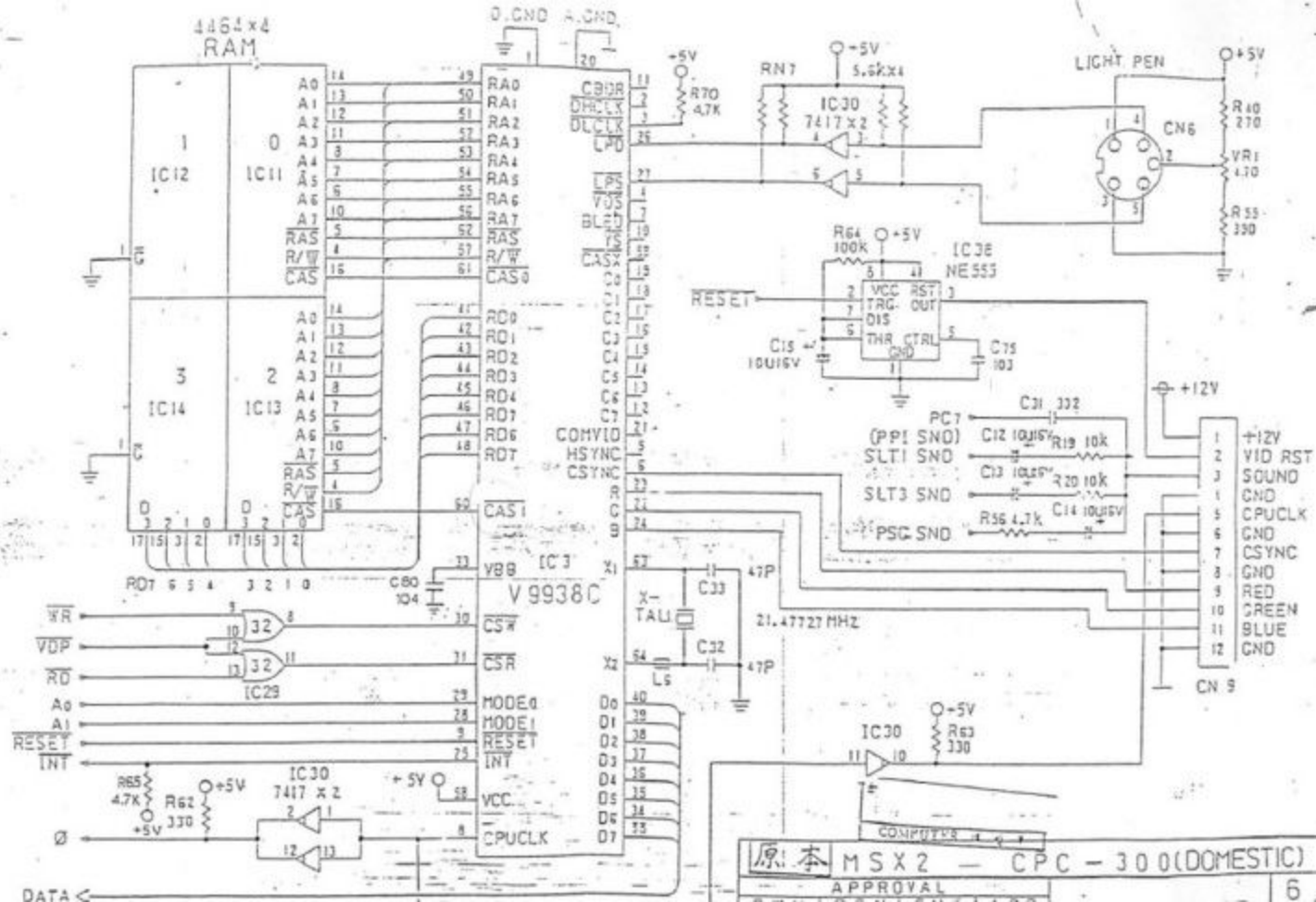


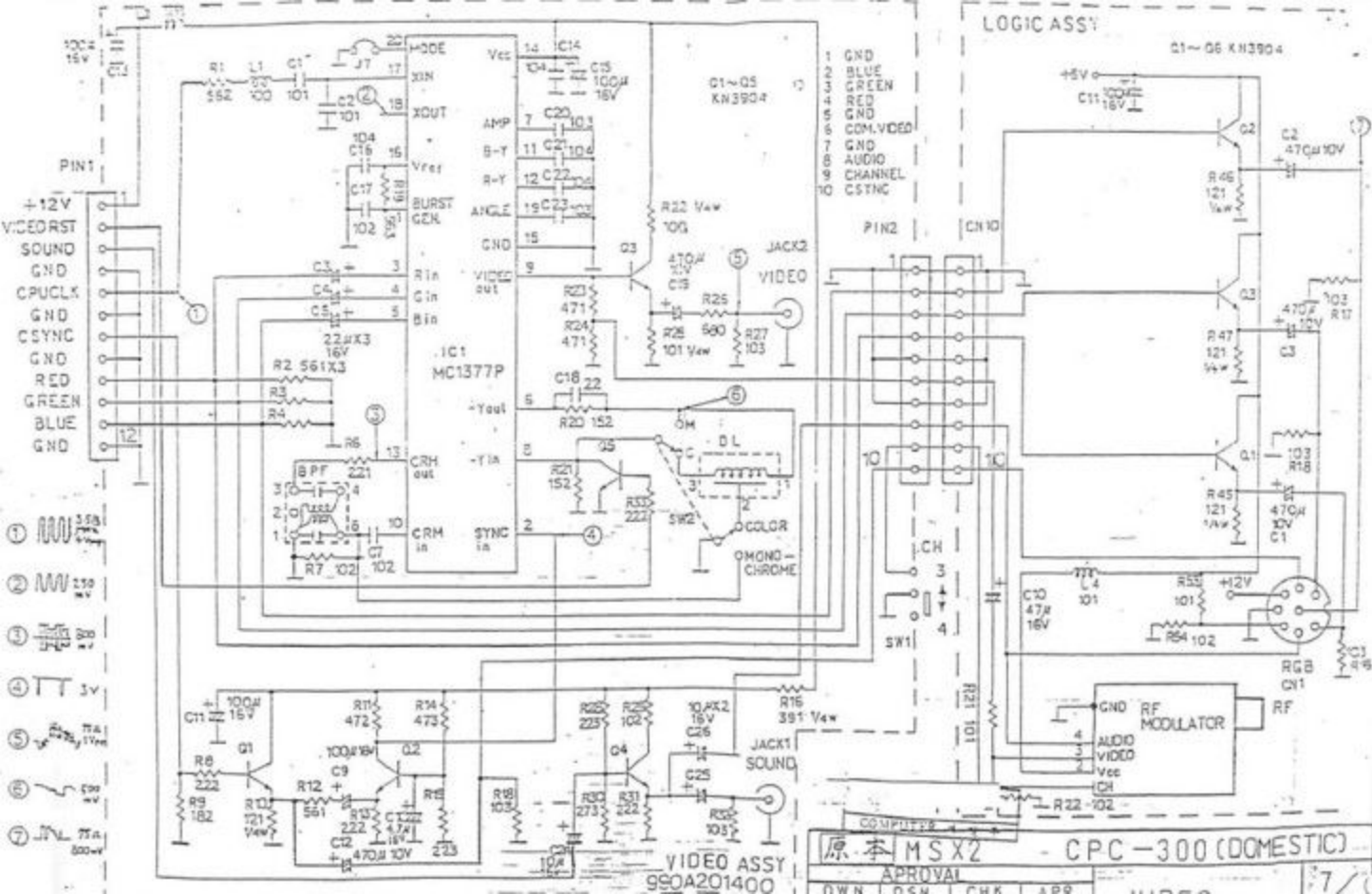
原 本 MSX2 C'PC-300(DOMESTIC)

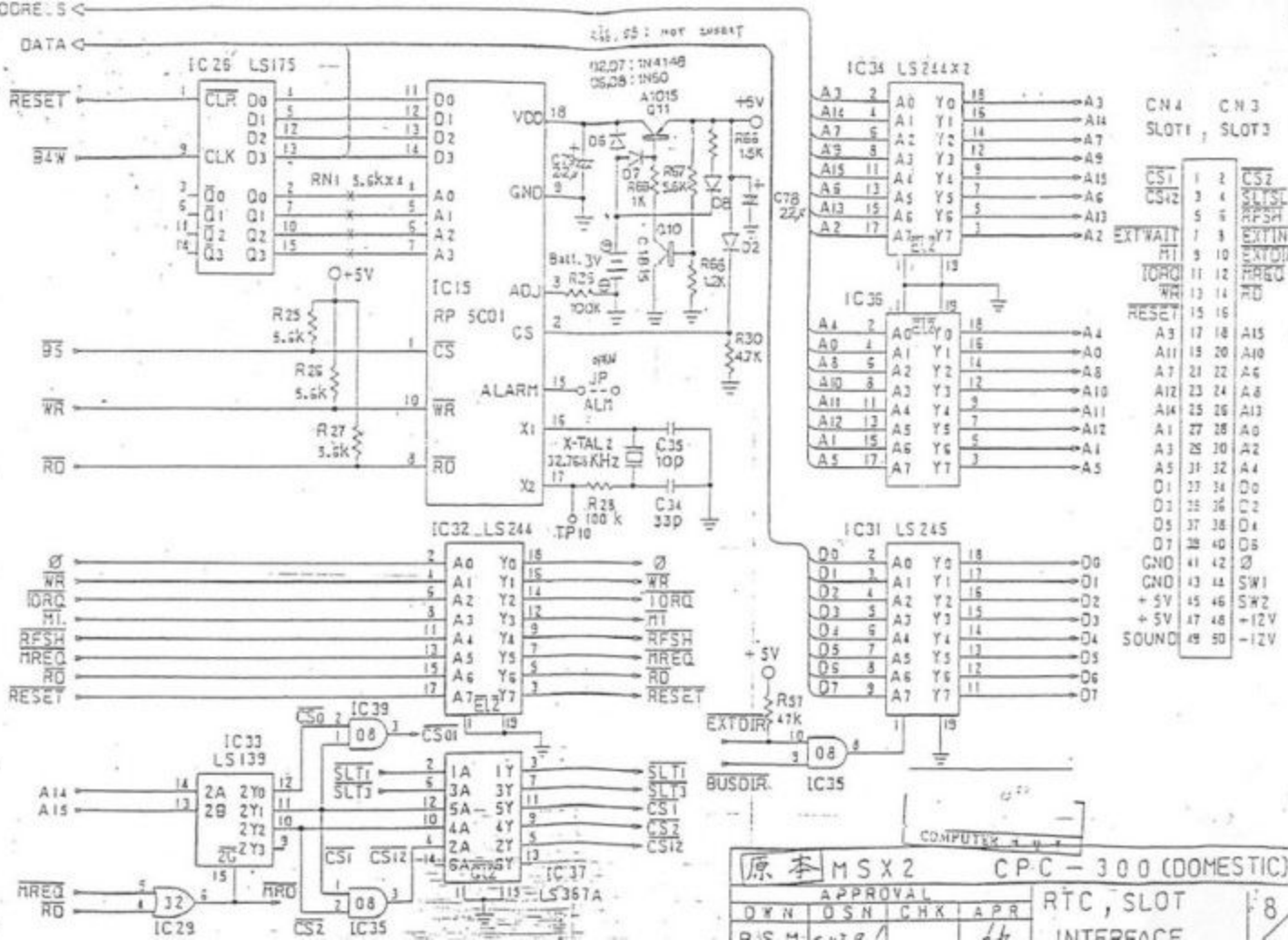
APPROVAL		
OWN	DSN	CHK
APR	CPU INTERFACE	

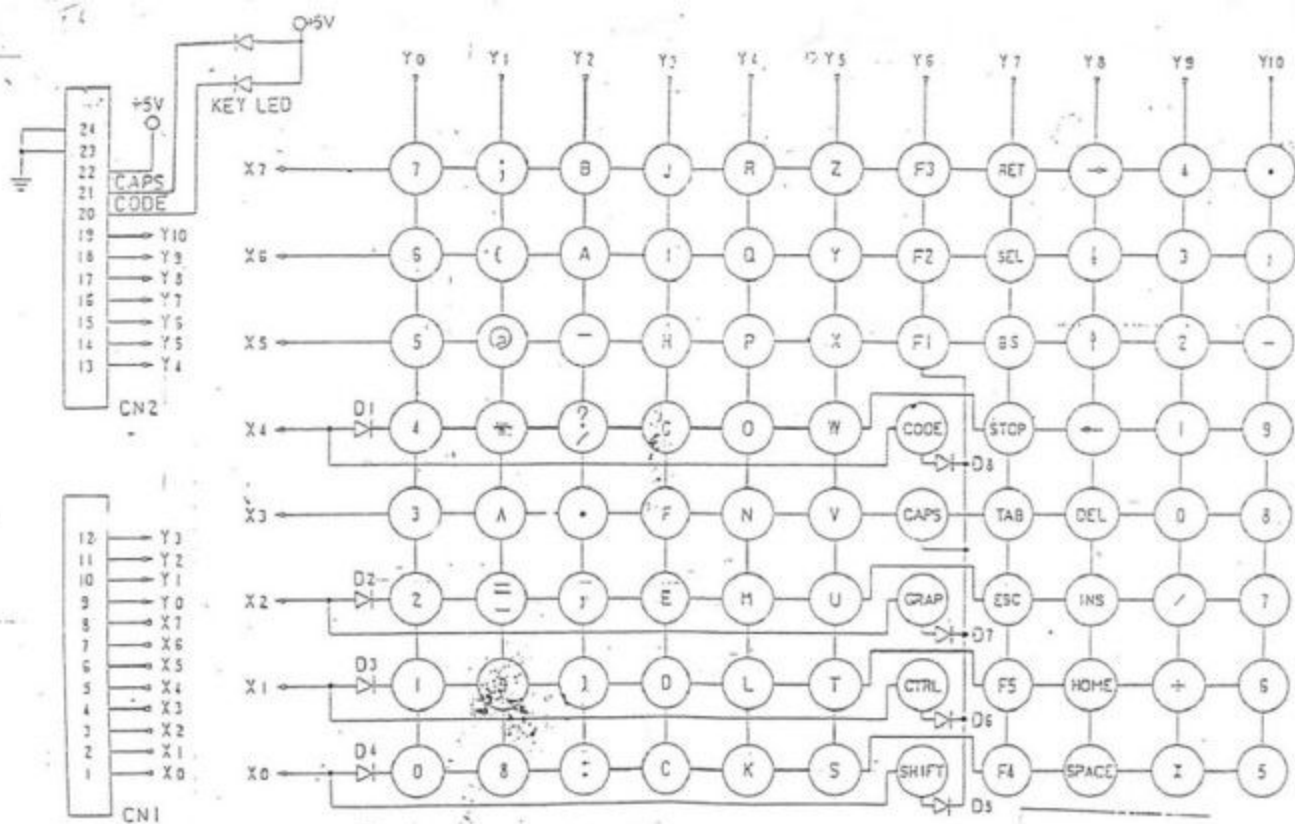












D1-D8: 1N4148